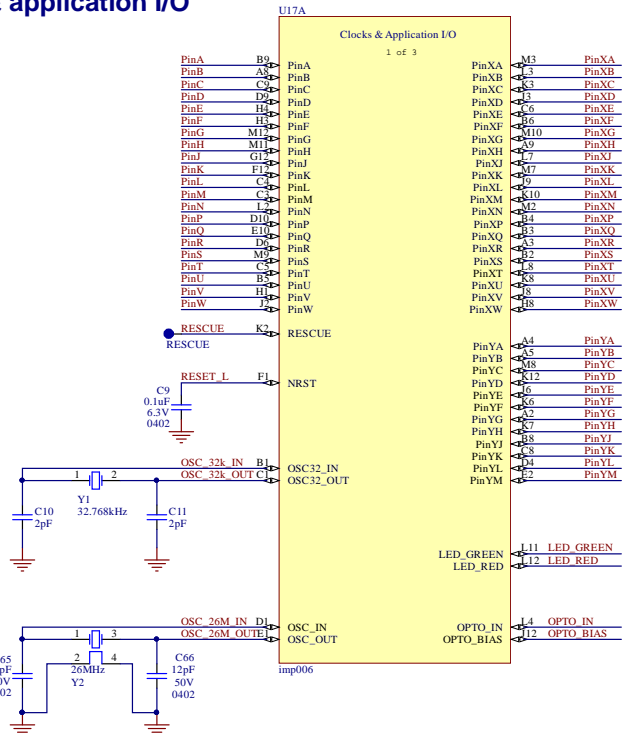
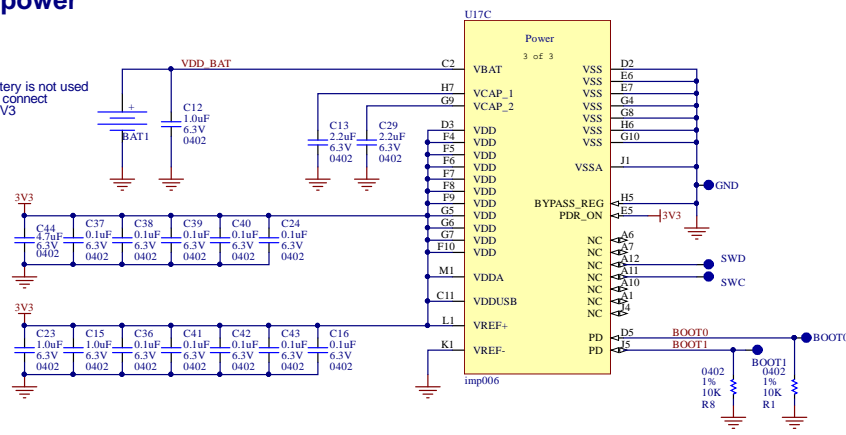


imp006 clocks & application I/O

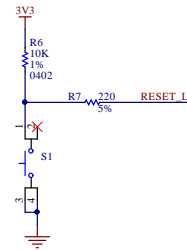


imp006 power

If a backup battery is not used in your design, connect VDD_BAT to 3V3

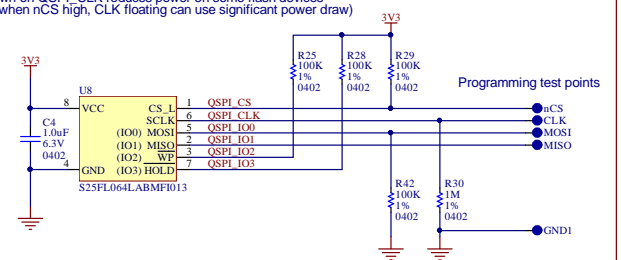


Reset switch



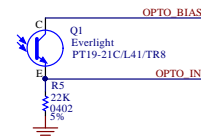
QSPI flash

Pulldown on QSPI_CLK reduces power on some flash devices (even when nCS high, CLK floating can use significant power draw)



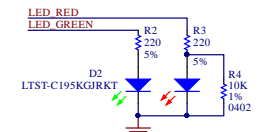
BlinkUp Phototransistor

Note: consult tuning guide on Electric Imp dev center to set resistor value in your own design

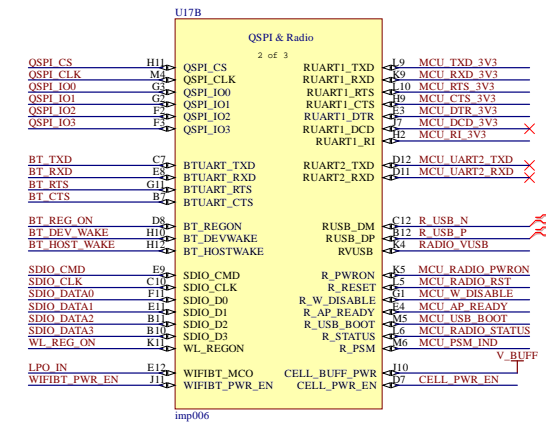


Status LED

imp006 supports both common anode and common cathode LEDs. The resistor across the red LED is used to auto-detect.



QSPI and radio connections



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Design: **imp006-breakout** Rev: **2.3** Electric Imp
5150 El Camino Real, Ste C-31
Los Altos, CA 94022
Sheet: 1 of 5 Date: 9/25/2019 Time: 4:13:23 PM
File: C:\Users\Aruna\Documents\Alum\Projects\imp006-breakout\rev2-2-alum-20190910\imp006-breakout\MCU-Imp-Sch-Sheet1.dwg

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Power selection jumpers

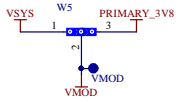
The imp006-breakout is designed for extreme power savings in multiple operating modes

W5: VMOD select

W5 determines how VMOD, the radio power supply (both cellular and WiFi/BT), is provided.

1-2: Power VMOD from VSYS, the Li-Ion PMU system output. This is used when a Li-Ion rechargeable cell is powering the system

2-3: Power VMOD from the 3v8 buck, which is used when a primary battery pack is in use.

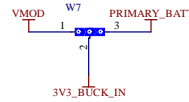


W7: 3V3 select

W7 determines how 3V3, the imp MCU power supply is provided.

1-2: Power 3V3 from VMOD, which could be VSYS or the primary buck. This is used when a Li-Ion cell is in use or a primary cell of >10V (the highest acceptable input for the 3V3 buck) is used.

2-3: power the 3V3 buck direct from the primary cell. ONLY USE THIS WHEN THE PRIMARY CELL MAX VOLTAGE IS <10V

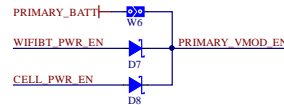


W6: Force buck on

W6 is used to force the primary buck ON, regardless of whether the MCU wants either radio to be powered.

This is used when W7 is set to 1-2, so that the primary buck will run continuously to provide the MCU with power, or when power needs to be preserved to the BG96 (eg for GPS state preserve).

If the primary cell used is <10v (and W7 is hence set to 2-3), the primary buck will only run when required, reducing Iq



Common configurations

Li-Ion rechargeable cell

W5: 1-2, W7: 1-2, W6: not fitted

Primary cell, 4-10v

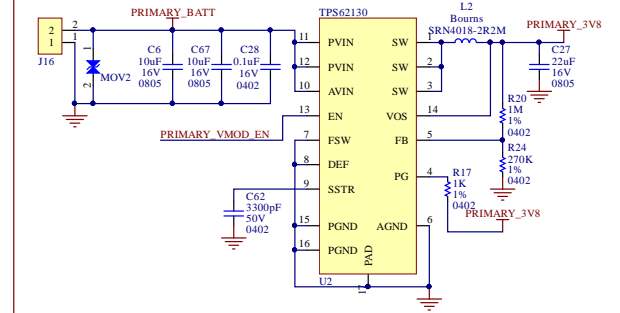
W5: 2-3, W7: 2-3, W6: not fitted

Primary cell, 10v-17v

W5: 2-3, W7: 1-2, W6: fitted

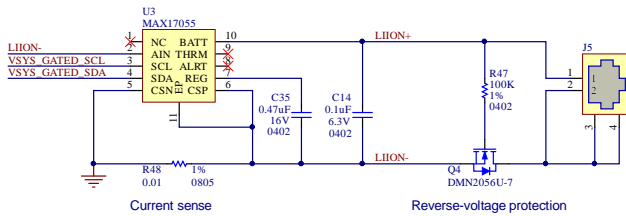
Primary battery 3V8 supply

Input range -4v-17v



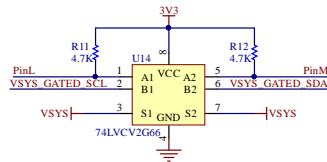
Li-Ion battery & gas gauge

8-bit I2C address: 0x6C



I2C bus isolator

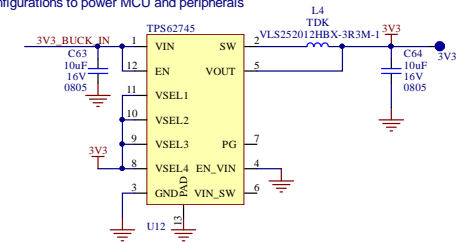
This ensures an unpowered PMU & GG do not load the I2C bus when a primary cell power source is used. Applicable only to this breakout board



Ultra-low Iq 3v3 supply

Input range -3.7v to 10v

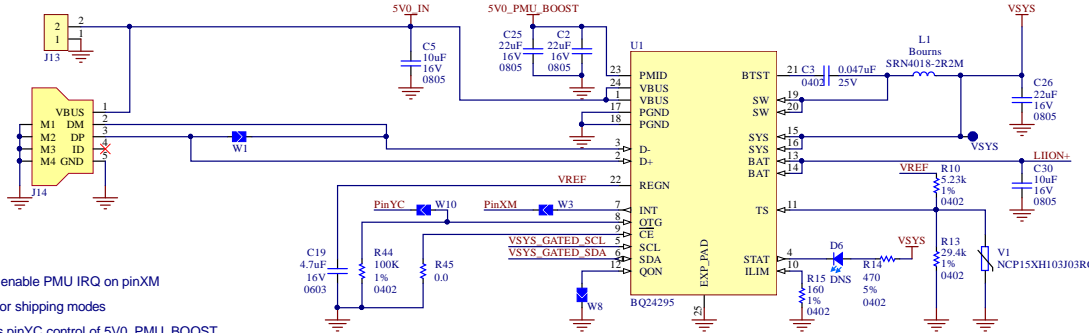
Used in all power configurations to power MCU and peripherals



Li-Ion PMU/battery charger & power path

8-bit I2C address: 0xD6

When J13 is used for power input, the USB DM/DP brick detect will not work, so short W1 to set 3A input current limit



Short W3 to enable PMU IRQ on pinXM

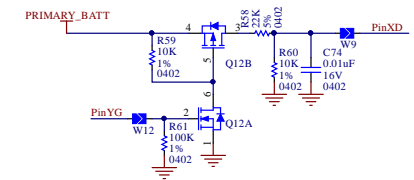
W8 is used for shipping modes

W10 enables pinYC control of 5V0_PMU_BOOST

Primary Battery Voltage Measurement

-3.7v to 17v

To use, short W12 and W9, then drive pinYG high to read scaled voltage on pinXD



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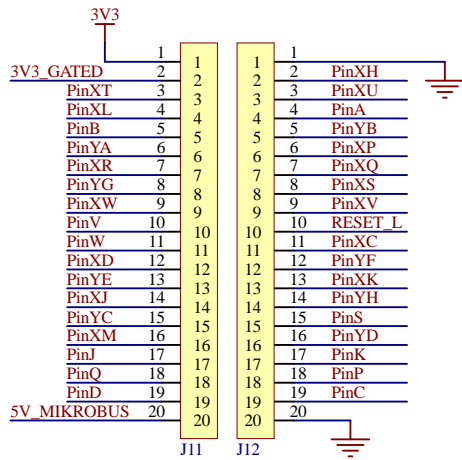
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Design: **imp006-breakout** Rev: **2.3** Electric Imp
5150 El Camino Real, Ste C-31 Los Altos, CA 94022
File C:\Users\Aruna\Documents\Alum\Projects\imp006-breakout\rev2.2-alum-20190910\imp006-breakout-power3.pdf

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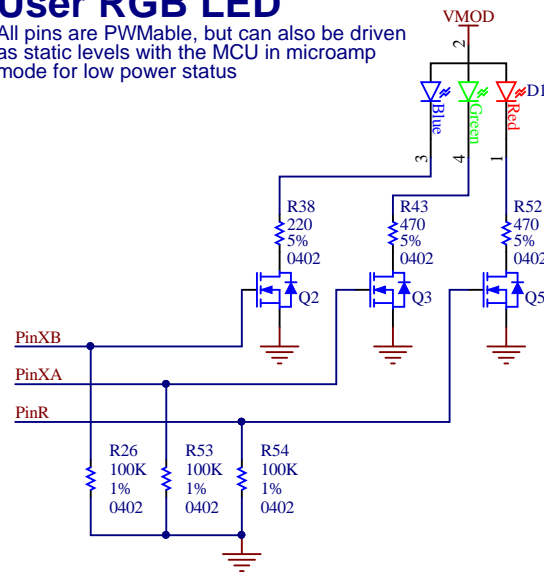
Breakout Connector

0.1" pitch 2x20 header



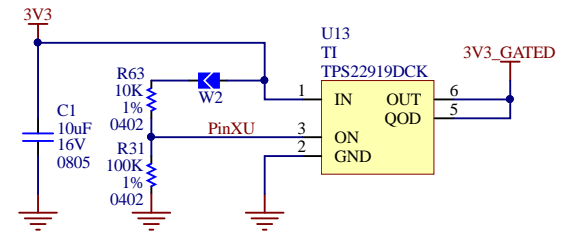
User RGB LED

All pins are PWMable, but can also be driven as static levels with the MCU in microamp mode for low power status

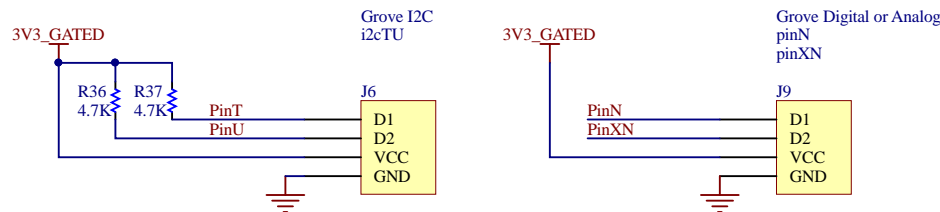


Power gate for Grove/Mikrobus

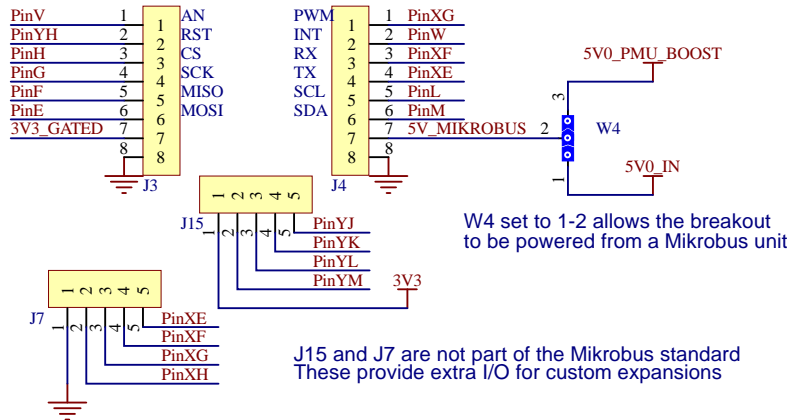
TPS22919 used to control inrush currents
Short W2 to force 3V3_GATED on
(if gated peripherals need to be on during deep sleep)



Grove connectors for I2C and digital/analog

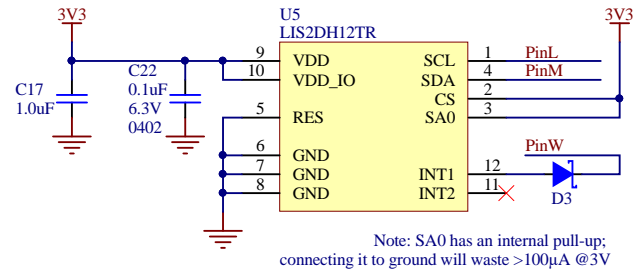


Mikrobus socket



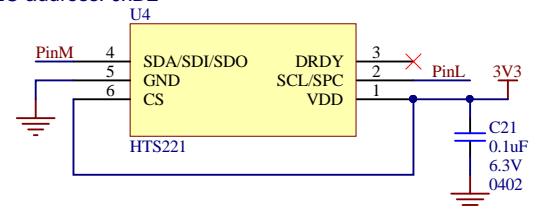
3-Axis Accelerometer

8-bit I2C address: 0x32 (SA0 High)
INT1 can wake imp from deep sleep via D3



Temperature & Humidity sensor

8-bit I2C address: 0xBE



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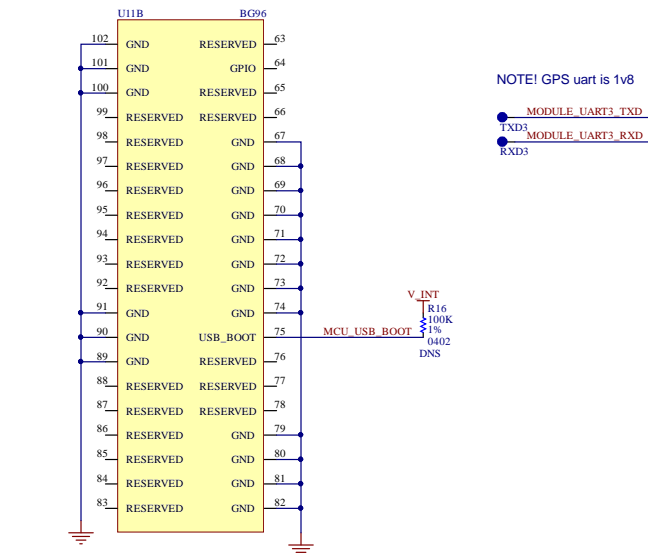
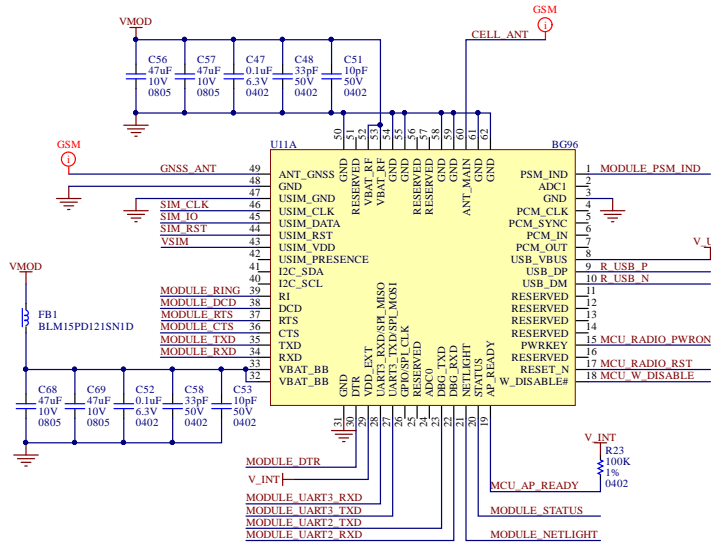
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Design: imp006-breakout	Rev: 2.3	Electric Imp 5150 El Camino Real, Ste C31 Los Altos, CA 94022	electric imp
Sheet: 3 of 5	Date: 9/25/2019	Time: 4:13:24 PM	
File: C:\Users\Agung Mandala\Documents\Altium Projects\imp006-breakout-rev2.2-altium-20190910\imp006-breakout-connector			

Quectel BG96 Cat-M, NB-IoT, 2G & GNSS module

Note all I/Os are 1.8V

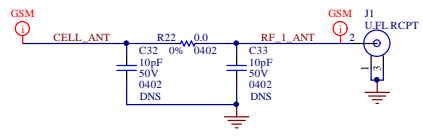


NOTE! GPS uart is 1v8

- MODULE_UART3_TXD
- MODULE_UART3_RXD
- RXD3

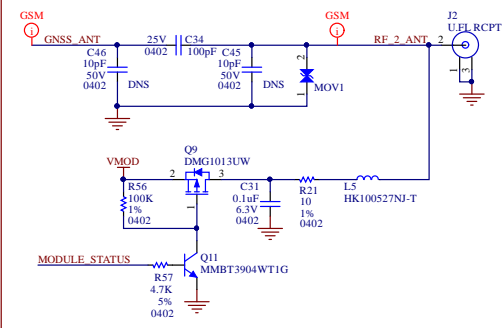
Cell antenna

Match circuit not stuffed, assumes 50 ohm antenna



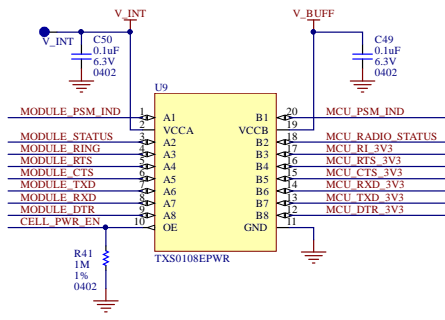
GNSS antenna

Match circuit not stuffed, assumes 50 ohm antenna
GPS antenna power is gated by MODULE_STATUS, so is only on when module is powered and awake



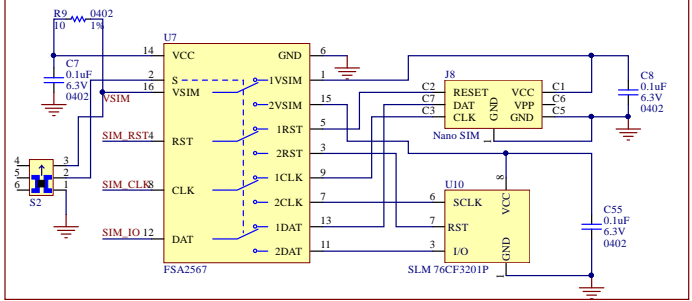
1v8 <=> 3v3 level shifter

TXS0108 performs bidirectional level translation
NOTE: This part is very noise sensitive. Attaching a scope to what should be a driven output often causes unpredictable behavior



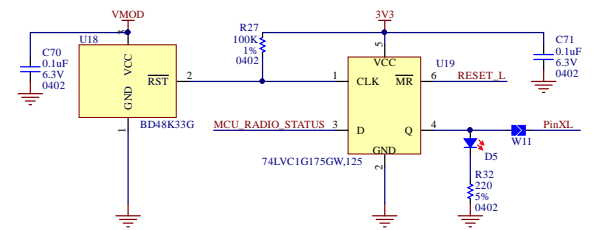
SIM select circuit

S2 switches all SIM lines between U10 (eSIM) and J8 (nano SIM socket)
Control signal must never float hence SPDT switch



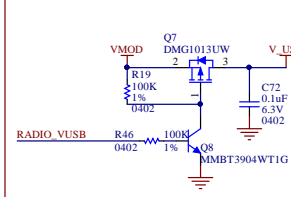
Radio brownout indicator

This circuit detects when VMOD drops below 3.3v when the radio is powered
If this happens, D5 is illuminated, indicating a possible supply issue. The indication is cleared by a reset
This usually indicates insufficient current available, especially on 2G networks
If W11 is shorted then the MCU can read this state in software



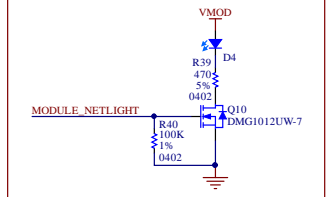
Radio VUSB gate

This circuit supplies V_USB from VMOD instead of 3V3
This provides extra margin against accidental USB disconnect if 3V3 is glitched



Network status LED

Searching for network: short on/long off
Registered on network: long on/short off
Data transfer: fast blinking



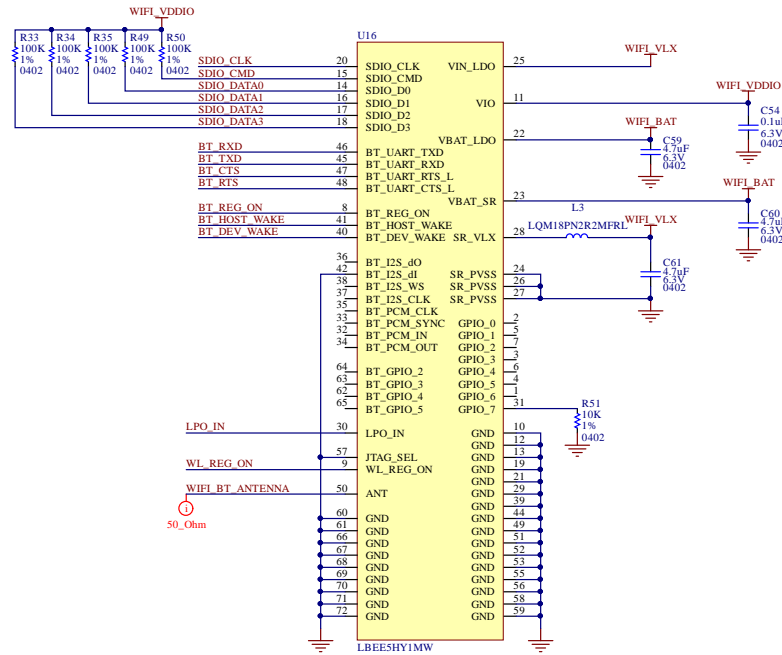
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Dual-band WiFi & BT module

The Murata LBEEESHY1Mw module is used to provide 2.4 & 5GHz WiFi ac/a/b/g/n and Bluetooth capabilities.

Please contact Electric Imp for guidance on using the Murata 1DX module if only single-band WiFi & BT is required.

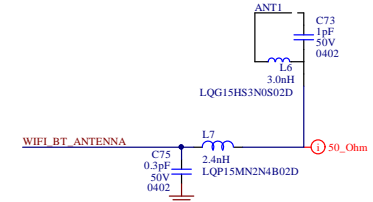
If you are not using WiFi or BT in your design, this entire page can be omitted.



Dual-band WiFi & BT antenna

The on-board antenna is the Murata reference antenna, which was used for their FCC modular certification testing. Due to layout constraints, this antenna is not used in the best configuration on the imp006-breakout.

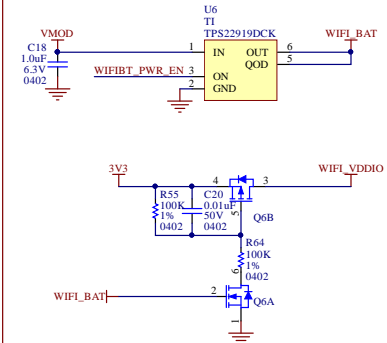
Please contact Murata for more details on correct 1Mw antenna placement and usage



WiFi / BT power gate

In this design, because someone may be using the imp006 breakout just for cellular, WiFi / BT power is gated totally when WIFIBT_PWR_EN is low; this saves 11uA for designs that aren't using WiFi or BT.

WiFi_VDDIO is gated by WIFL_BAT, ensuring that it comes up after WIFL_BAT as specified in the CYW43455 datasheet.



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Sheet: 5 of 5 Date: 9/25/2019 Time: 4:13:25 PM
File: C:\Users\Acuna.Mandala\Documents\Alum Projects\imp006-breakout-rev2-2-atom-20190910\imp006-breakout-type.tn

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