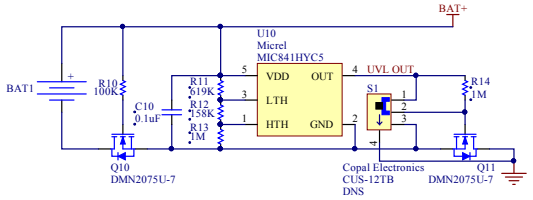
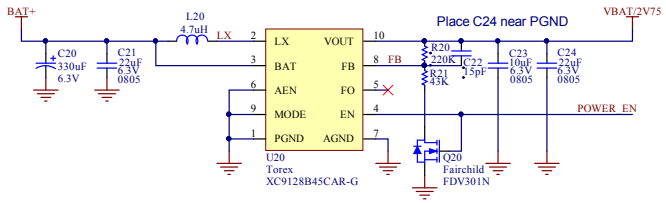
Reverse Voltage Protection and Under Voltage Lockout

Q10 provides efficient protection in case of battery reversal. Make sure to use a NFET with a low VGS_TH. U10 and Q11 provide an undervoltage lockout for graceful shutdown when the batteries can no longer supply enough power to the sensors. The lock out is set at 1.9V and re-enable will occur at 2.2V.

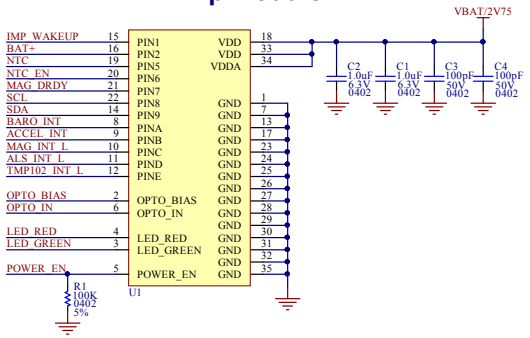


Power Supply

We chose the XC9128 because it is low cost, efficient, has a low Iq and has passthru when it is disabled. To save power when it is disabled we also turn off the leakage path through the FB network. Alkaline batteries can have a very high output impedance so we recommend at least 220uF on the battery input to support high current peaks.

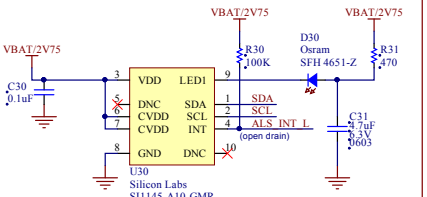


Imp Module



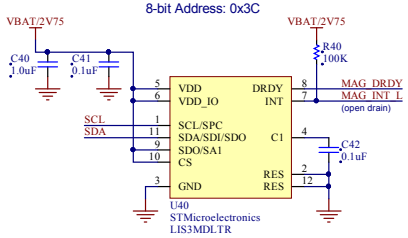
Digital Ambient Light Sensor

8-bit Address: 0xC0



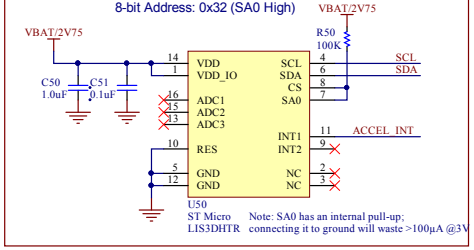
Magnetometer

8-bit Address: 0x3C



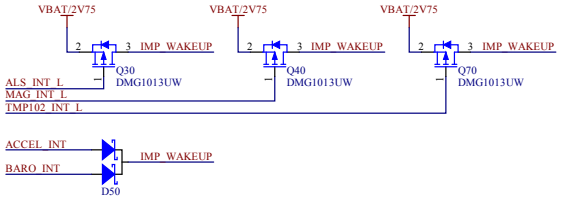
3-Axis Accelerometer

8-bit Address: 0x32 (SA0 High)



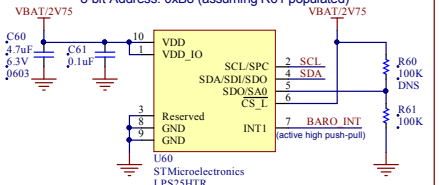
Interrupt ORing Circuit

The Imp can only wake on a logic high. Most sensors in this design can be configured for that polarity, but default to active low and use an open drain topology - causing them to burn power while low. For that reason, we use PFETs to invert the logic and combine those interrupts (and the barometer and accelerometer's active high interrupts, using diodes) en route to Pin 1.



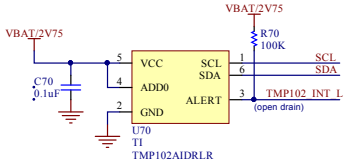
Air Pressure Sensor

8-bit Address: 0xB8 (assuming R61 populated)

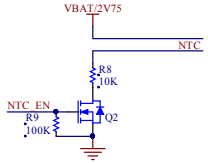


Cheap I2C Temperature Sensor

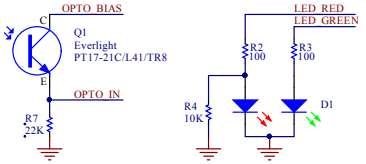
8-bit Address: 0x92 (ADD0 High)



NTC Thermistor Pads

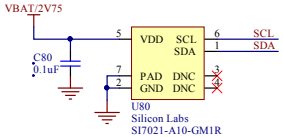


BlinkUp Components

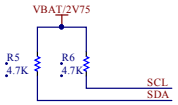


Temperature+Humidity Sensor

8-bit Address: 0x80



I2C Pull-up Resistors



Mounting Hole

Design: **Impee Nora**	Rev: **5.0**	Electric Imp 5050 El Camino Real, #221 Los Altos, CA 94022
Sheet: 1 of 1 Date: 1/7/2015 Time: 12:15:11 PM File:Z:\ei-hardware\impee-nora\impee-nora.SchDoc		electric imp